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VLSI Digital Signal Processing Systems: Design and Implementation, Keshab K. Parhi, ISBN: 978-0471241867, Wiley, 1999. Suggested references The Design Warrior's Guide to FPGAs, Devices, Tools and Flows. Clive "Max" Maxfield, ISBN: 0750676043 Digital Signal Processing with Field Programmable Gate Arrays, Uwe Meyer-Baese, 3rd

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He joined IIT, Kharagpur as a faculty member in 1994, where he currently holds the position of a professor in Electronics and Electrical Communication Engg. The teaching and research interests of Prof. Chakraborty are in Digital and Adaptive Signal Processing, VLSI Signal Processing, Linear Algebra and Compressive Sensing.

### NOC | VLSI Signal Processing

1. " VLSI Digital Signal Processing Syustems", Keshab K. Parhi, Wiley Eastern 2. " Digital Signal Processing for Multimedia Systems", Keshab K. Parhi and Takao Nishitani, Marcel Dekker. 3. " Pipelined Lattice and Wave Digital Recursive Filters", J. G. Chung and Keshab K. Parhi, Kluwer.

### VLSI Signal Processing - Course

Journal of VLSI Signal Processing, 9, 121-143 (1995), Kluwer Academic Publishers, Boston. Keshab K. Parhi and David G. Messerschmitt, Pipeline Interleaving and Parallelism in Recursive Digital Filters - Part I: Pipelining Using Scattered Look-Ahead and Decomposition.

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